

Design of Low-Voltage, Low-Power Operational Amplifier Cells (The Springer International Series in Engineering and Computer Science)

Ron Hogervorst, Johan Huijsing



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Design of Low-Voltage, Low-Power CMOS Operational Amplifier Cells describes the theory and design of the circuit elements that are required to realize a low-voltage, low-power operational amplifier. These elements include constant-gm rail-to-rail input stages, class-AB rail-to-rail output stages and frequency compensation methods. Several examples of each of these circuit elements are investigated. Furthermore, the book illustrates several silicon realizations, giving their measurement results.

The text focuses on compact low-voltage low-power operational amplifiers with good performance. Six simple high-performance class-AB amplifiers are realized using a very compact topology making them particularly suitable for use as VLSI library cells. All of the designs can use a supply voltage as low as 3V. One of the amplifier designs dissipates only 50µW with a unity gain frequency of 1.5 MHz. A second set of amplifiers run on a supply voltage slightly above 1V. The amplifiers combine a low power consumption with a gain of 120 dB. In addition, the design of three fully differential operational amplifiers is addressed. *Design of Low-Voltage, Low-Power CMOS Operational Amplifier Cells* is intended for professional designers of analog circuits. It is also suitable for use as a text book for an advanced course in CMOS operational amplifier design.

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